

WovenCircuits: A 3-Step Fabrication Process for Weaving Electric Circuit Layouts in Everyday Artefacts

Ahmed Awad*
Queen's University
iStudio Lab
Kingston, Ontario, Canada
a.awad@queensu.ca

Salma Ibrahim*
Queen's University
iStudio Lab
Kingston, Ontario, Canada
salma.ibrahim@queensu.ca

Sara Nabil
Queen's University
iStudio Lab
Kingston, Ontario, Canada
sara.nabil@queensu.ca



Figure 1: We explore WovenCircuits, a fabrication process to design seamlessly-integrated circuit layouts using digital weaving (right). Adopting a Research through Design (RtD) approach, we contribute a 3-step process (top) and a series of interactive research products that extend the functional and aesthetic dimensions of woven e-textile designs (bottom): a) PowerPocket Jeans for wireless charging; b) Thermo-Placemat for warming food; c) GoGreen Backpack for embodied expression; d) VibroChair Cover for posture correction; e) SensingRug for activity recognition; and f) ThirstyCat Tapestry for tactile animation.

Abstract

Previous work explored techniques for creating woven e-textiles, emphasizing interactive input and output elements. However, the integration of electrical connections and circuitry remains underexplored. Using Research through Design (RtD), we present WovenCircuits, a design-led inquiry into combining traditional weaving methods with computational design on digital Jacquard looms to create woven circuit schematics. Through iterative design experiments, we developed a 3-step process and characterized three fabrication techniques to: 1) weave insulated electrodes, 2) integrate rigid components into fabric, and 3) create woven electrical connections. We further examined their electrical behaviour through key design factors and evaluated the effect of washability on resistance and dimensions. To demonstrate its potential, we designed and built six high-fidelity research products showcasing diverse applications in interactive everyday objects. Finally, we reflect on the design opportunities and limitations of WovenCircuits, contributing to the growing body of knowledge on woven e-textiles.

*Both authors contributed equally to this research.

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CCS Concepts

• Human-centered computing → Human computer interaction (HCI).

Keywords

weaving; e-textile; circuits; fabrication; hybrid craft; research through design; everyday things

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1 Introduction

E-textiles can be created using conductive yarns, fibres, and fabrics with a variety of crafting methods, including knitting [2, 52, 56], embroidery [14, 48, 62, 86], spinning [35, 49], crocheting [26, 67], and weaving [73, 78]. This enables the seamless integration of interactivity into everyday garments and objects as envisioned in Interioraction [64] and Decoraction [63]. Prior work has explored weaving as a structural-level integration approach, implemented at the crafting stage as opposed to post-production surface-level methods, resulting in slimmer, sturdier, and more seamless integration [12, 13, 19, 23, 31, 46, 57, 73, 78, 80]. We adopt a Research through Design (RtD) [90] approach to explore weaving electric

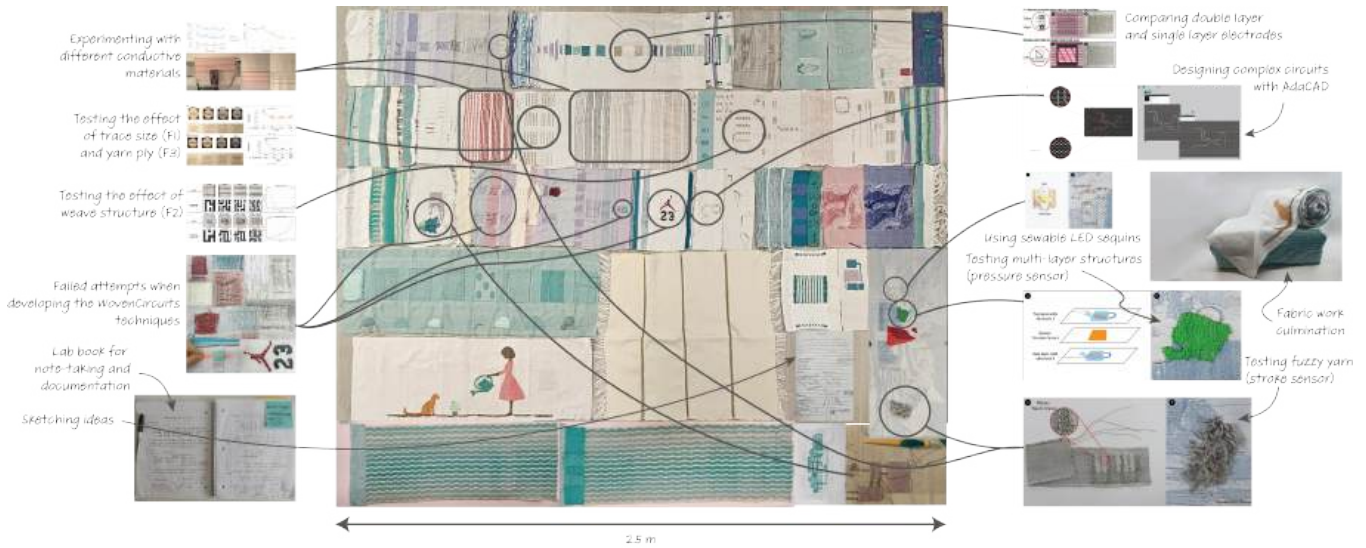


Figure 2: Collage of woven pieces of fabric and annotations in the lab book with sketches and the roll of fabric woven during the experimentation and research-through design prototyping phase. We show failed attempts as well as samplers, quantitative experiments of characterization, and some early iterations of research products.

circuit layouts for integrating rigid electronic components within interactive everyday things (see Figure 1).

Compared to other crafting methods, the main advantage of weaving lies in how the yarn is manipulated to create fabric and how the design process can be streamlined through computational design and machine-assisted prototyping. Weaving operates by interlacing vertical threads (warp) with horizontal threads (weft), producing fabric as a matrix of intersections between warp and weft. By controlling the interlacement pattern at the pixel level using digital looms, weavers can create unique multi-layered woven structures, enhancing the design potential of woven e-textiles [13, 73, 84]. Subsequently, we were motivated to explore how integrated circuits can be created using various weaving structures to achieve different effects and functionality [13, 78]. Researchers have made significant strides in developing software tools to help design weaving patterns and drafts for woven e-textiles and sensors [16, 20]. However, comprehensive guides for digitally weaving e-textiles are notably scarce. This prompted us to conduct design research to develop a formalized fabrication process for designing complete woven circuit layouts that build on techniques from existing literature and traditional weaving methods for HCI researchers, makers, and practitioners of e-textile prototyping.

Woven e-textiles offer many opportunities and new affordances for interactive devices. However, there are two main constraints that limit the weaving of complex circuits. These limitations hinder the creation of deployable wearables and woven everyday things. First, component placement is restricted to either the vertical or horizontal axis. This can present a significant challenge when weaving complex circuit layouts, particularly those with intricate pathways required to ensure proper connectivity. Second, the weaving process involves interlacing the weft yarn at each step. In cases where circuits require two or more separate parallel vertical conductive

pathways, sharing the same conductive weft will cause the pathways to be connected. Throughout this paper, we will often refer to these conductive pathways and connections as ‘traces’.

Inlay weaving is the technique of introducing a supplementary weft (an additional bobbin of yarn that is distinct from the warp and weft) to create patterns or designs, on specific locations of the fabric, that stand out from the main weave structure (warp and weft) [74]. The programmable nature of inlay weaving on Jacquard looms, where individual warp threads can be controlled, can offer a partial solution to these constraints by selectively incorporating conductive thread wefts to form specific shapes and create conductive traces. This contrasts Dobby looms, where inlay weaving would be an entirely manual endeavour since the warp threads in these looms are controlled in groups [74]. Current methods for creating woven sensors using inlay weaving on digital Jacquard looms [14] result in single-layer woven electrodes, which may not be ideal for wearable applications. Industrial looms address inlay weaving in a sophisticated and automated fashion, but they are typically inaccessible to maker spaces and unsuited for small-scale e-textile prototyping. This highlights the need for exploring an approach to weaving circuits that can be tailored to specific applications. Multi-layered weaving approaches [13, 74, 78] when combined with inlay weaving through the use of a supplementary weft is a promising technique that we will demonstrate through this paper. In addition, there is a notable gap in research addressing the implementation of complete electronic systems that integrate all the necessary functional elements, including interactive components, microcontrollers, power sources, and electrical connections.

In this paper, we present WovenCircuits, a weaving approach that builds on existing techniques to streamline the creation of complex woven circuit schematics, through the following contributions:

- (1) A 3-step process and three fabrication techniques for weaving electric circuit connections and layouts, referred to as WovenCircuits (T1: weaving insulated electrodes using double-layer Ghost Inlays; T2: integrating electronic components into single-sided pockets; and T3: weaving electric connections with conductive thread traces and wire tunnels).
- (2) A characterization of the electrical behaviour and washability of WovenCircuits, along with the identification and evaluation of three key design factors (F1: trace size; F2: weaving structure; and F3: yarn ply).
- (3) Validation of WovenCircuits through six fully interactive, high-fidelity research products. These deployable artefacts showcase the workflow of our 3-step process, as well as the flexibility of mixing and matching the three fabrication techniques of WovenCircuits.

2 Related Work

2.1 Weaving E-textiles in HCI

E-textiles, or electronic/smart textiles, incorporate interactivity through textile-based sensing, functional, or actuating elements. These elements can be integrated using traditional handcraft techniques, such as sewing [65], crocheting [67], and punch-needling [28, 39], or through machine-assisted methods like knitting [2, 52, 85], embroidery [9, 48, 59, 62], and weaving [79]. Research has shown that weaving can enable the integration of interactivity during the creation of the fabric itself [79], offering customizable benefits and aesthetic richness [46].

However, the design phase for weaving, particularly for complex structures [88], can be challenging and less intuitive. This difficulty partly arises from the gap between the design process and the resulting woven fabric, since a 2D weaving draft can be designed to produce a multi-layered woven structure [16]. Prior work [12] addresses this issue by offering a beginner-friendly introduction to paper weaving in a simple activity book format. Another approach to bridging this gap [10] involves employing multiple methods, such as diagrams and paper models, to create a more intuitive design process for multi-layer weaving [78]. Inspired by the community's interest in simplifying e-textile weaving, we introduce WovenCircuits, which includes a straightforward 3-step fabrication process and enables designers to implement fully interactive objects and high-fidelity projects through three main fabrication techniques that act as building blocks for woven circuit designs.

2.2 Digital Weaving

Weaving produces fabric by interlacing vertical (warp) with horizontal (weft) yarns [13]. Hand weaving [76] involves predominantly manual processes, offering extensive customizability and flexibility, but can be time-consuming and labour-intensive [76]. However, digital weaving [68] automates much of the process, providing various levels of computational flexibility. While industrial machines are more expensive and offer less customizability than hand weaving [16], some digital Jacquard looms (such as the TC2 [66, 83]) provide hybrid capabilities with digital control and manual real-time customization, making them ideal for e-textile prototyping.

Previous work [3, 16, 17, 20] introduced digital design software tools for weavers, which translate the abstract binary representation of digital weaving drafts into more intuitive formats. For instance, AdaCAD [16, 20] is an open-source weaving draft design tool developed by (and for) HCI research. It empowers designers to utilize various weaving methods and techniques through parametric design components. Building on recommendations of recent work [12, 20], our WovenCircuits fabrication techniques utilize the capabilities of a TC2 digital Jacquard loom and the AdaCAD design software to customize circuit designs.

2.3 Woven E-Textile Circuits

2.3.1 Electrical Connections. Planning connection traces, also known as circuit routing [22], aims to achieve stable and secure electrical connections between circuit components. Previous work [18, 40, 44] on woven connections follow two main approaches for routing. The most common approach involves designing a grid of connectors, with the horizontal connectors aligned to the weft and vertical ones aligned to the warp [54, 55, 91]. The aim of this is to: 1) enable a uniform distribution of components since the spaces are uniform across the area of implementation [32, 91]; and 2) utilize the weaving structure to ensure proper insulation between adjacent connectors [55]. The other approach implements connections as inlays, which allows the wire to be freely woven between two points with no constraints on direction [30, 31, 78]. Although the latter approach provides more flexibility in routing and placement of components, it requires longer paths, degrading the conductivity of the trace. On the other hand, recent work in HCI research [88], discusses fully integrated woven circuitry as a research gap for future work, and highlights the opportunity to expand the woven circuit to integrate multi-material weaving practices into e-textiles.

2.3.2 Wire Materials. Different materials can be used to form connections, such as insulated copper-clad aluminium wires [31, 54]. The insulation adds an additional layer of protection against electrical issues. However, using rigid metallic wires as wefts negatively impacts the drapability and texture of the fabric [54]. While used in prior work [30, 31], this method can also lead to discrepancies in the woven structure [75], potentially compromising the electrical stability of the circuit [30, 91]. Alternatively, metallic or conductive yarns provide another option for weaving connection traces. These yarns are typically produced by wrapping a non-conductive, flexible core with a conductive metal [72]. One advantage of conductive yarn over insulated wires is its flexibility and texture, allowing it to blend seamlessly into the substrate structure and achieve more ubiquitous integration [10]. Recent research has explored options for fabricating biodegradable conductive threads [89] for low-fidelity e-textile prototyping. However, these environmentally conscious and sustainable practices face challenges such as replicability, lack of standardization, and inconsistent performance due to reliance on locally sourced materials and varying climate conditions. In our research comparing commercially-available options [5], we experimented with several types of conductive threads, including Adafuit stainless steel, silver-plated Karl Grimm, enamelled copper, and silver-plated Madeira. Among these, the silver-plated Madeira HC-12 ($<100 \Omega/m$) provided the most reliable functional and aesthetic results for high-fidelity projects.

T1: Weaving insulated electrodes by double-layer Ghost Inlays

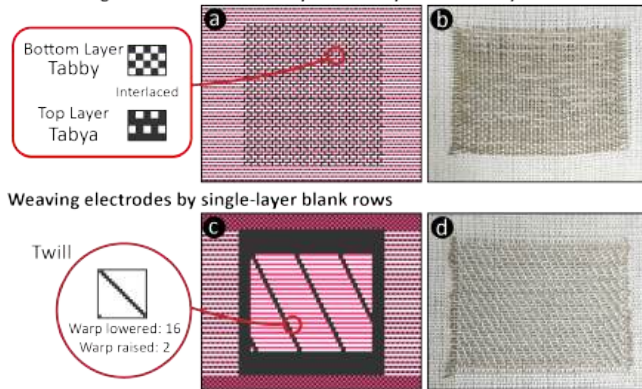


Figure 3: Electrode weaving with conductive thread: a) draft using T1 as a double-layer Ghost Inlay; b) woven fabric electrode using T1 (insulated from the back); c) draft using AdaCAD tutorial; d) resulting woven electrode.

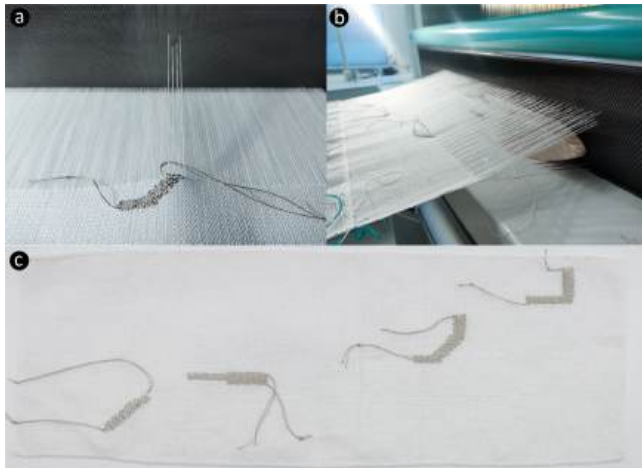


Figure 4: Using our T1 technique to weave a conductive thread electrode as a double layer Ghost Inlay showing the automation of the process alternating between the: a) top layer, b) bottom layer, and c) in a variety of angles and directions.

2.3.3 Circuit Components. Researchers have explored weaving e-textile sensors and actuators, but other electronic circuit components typically remain external to the fabric [13, 84]. One approach involves weaving a pattern with floating threads, which allows rigid parts to be placed through the *floats*, securing electronic components in place [24, 91]. Another approach is to position components between fabric layers, effectively forming a pocket to house them. Double weaving techniques can be used to form such structures, producing woven fabrics with two distinct layers [12, 13]. Prior work has also explored multi-layer weaving structures to store sensing components, serving as a protective shield against external hazards [58, 78, 84]. However, Jacquard double weaving in the middle of the fabric (away from the selvage) creates enclosed gaps, similar to closed pockets, once they are completely woven. This

approach limits post-weaving integration of electrical components and offers minimal flexibility for troubleshooting and debugging. Researchers have suggested that future work should consider creating “flexible PCBs that can be customized into 2D shapes that align with the design [...] to create robust yet seamless circuit integration” [88] or “distribute electronic components into circuit islands that can be more seamlessly woven into the textile structure” [79].

3 WovenCircuits Fabrication Techniques

Weaving, in its most general application, typically involves throwing the shuttle (i.e., the weft yarn bobbin) across the entire warp width. This presents significant challenges when weaving circuit layouts where parallel connection traces must be effectively insulated from each other to prevent issues such as short circuits or signal cross-talk. To address this, we utilized parametric design tools (AdaCAD [16, 20]) on a digital Jacquard loom (TC2 [66]). Our design research produced hundreds of samples that informed three fabrication techniques for weaving insulated electrodes (T1), multi-layer pockets for electrical components (T2), and electric connections (T3) through WovenCircuits.

Research Through Design (RtD) Approach. Adopting an RtD approach as proposed by Zimmerman et al. [90], our exploration of WovenCircuits engages with the wicked problem of how to seamlessly embed circuit layouts and electronic components into woven textile substrates. This understanding of RtD suggests that contributions should be evaluated based on the rigour of the design process, the novelty of the invention, its relevance in addressing the problem, and its extensibility—that is, the extent to which it can be built upon in future work. Our design research process involved weaving more than 15m of 36cm wide fabric (totalling 5m²) over the course of 18 months. This formed an annotated portfolio [21] of samplers, iterations of research products, failed attempts, and experiments (Figure 2). The following fabrication techniques, and subsequent characterization and fabrication workflow, emerged from this iterative process. Together, they form a collection of extensible methods that directly address the challenges of designing functional woven e-textiles, which we later demonstrate through six high-fidelity research artefacts.

3.1 T1: Weaving insulated electrodes using double-layer Ghost Inlays

To integrate electrodes into woven structures, conductive thread is commonly used as an inlay to form the conductive surface [78] (see Figure 3). Inlay weaving allows additional yarn, independent from the weaving structure, commonly referred to as a supplementary weft, to be manually integrated into the woven textile, enabling weavers to create intricate shapes [70]. Inlay weaving is considered labour-intensive and time-consuming [70], with a high barrier to entry for beginner weavers, especially on Dobby looms. Inlay weaving on Jacquard looms streamlines the process because individual warp threads can be lifted, allowing weavers to have more precise control over the placement of their supplementary weft [74].

The capabilities of computational weaving on Jacquard looms addresses this issue by introducing ‘blank rows’ at every other step, allowing the loom to alternate between the inlay warp ends and the background [16, 20]. However, this method creates the inlay on the

same layer as the rest of the fabric. Since both the background and inlay exist on the same layer within the electrode area, each row of conductive thread is followed by a row of insulating non-conductive thread, reducing the electrode's surface area and thereby degrading its efficiency (see Figure 3d). Additionally, this approach makes the electrode conductive on both sides of the fabric, which restricts its use in wearable applications due to the potential for false positive touches caused by contact with human skin.

To solve this, we built upon previous work on multi-layer weaving structures [74] by developing the technique of 'Ghost Inlays', which introduces a weaving structure that incorporates blank rows at every other step, hidden in a double weaving block we call 'Tabya', as seen in Figure 3a. This effectively combines double weaving with inlay weaving by instructing the loom to weave a bottom layer (the background and main weave structure) and a top layer (an inlay woven with the supplementary weft) at the same time (Figure 4). Implementing Ghost Inlays gives a clearer visual guide on where the electrode area is (Figure 4a) because only the warp threads of the top layer are lifted, compared to inlays with pick-up sticks (on Dobby looms). This technique also resolves the issue of single-layer inlays (on Jacquard looms), making it more suitable for wearable applications because the bottom layer insulates the conductive material on the top layer.

Following this approach, the Ghost Inlays technique has the benefits of being: 1) insulated from the back in a double-layer structure, which is more appropriate for wearable and on-skin interfaces; 2) more conductive, as there is more surface area of conductive thread on the top layer; 3) independent from the weft yarn used in the base fabric, allowing parallel conductive traces to be woven at the same step without sharing the same weft; and 4) more visually clear and customizable since the process is guided by the weaving draft, resulting in neater inlay and trace salvages.

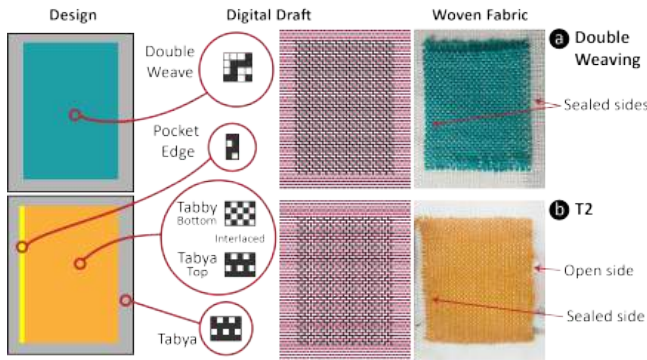


Figure 5: Woven pockets using: a) double weaving and b) the WovenCircuits T2 method as a single-sided pocket.

3.2 T2: Integrating electronic components into single-sided pockets

Pockets are areas with double weaving structures designed to double the fabric's width or hide objects within two or more layers [73]. These pockets can be employed to conceal electronic components effectively. Double weaving of closed pockets (with sealed edges on

all sides) requires the integration of components during the weaving process, limiting their maintenance, removal, or replacement post-weaving (see Figure 5a). To address this, we developed the 'single-sided pocket' technique.

The top and bottom edges of a pocket (parallel to the weft) are sealed in all cases due to the continuous interlacing of the warp threads, which connect the edges of the top and bottom sides to the background layer. Applying our 'Tabya' block to the area generates open sides by default for the weft-parallel sides as the outermost warp thread is isolated from adjacent warp threads, preventing their interlacement and thus creating openings. To control whether a side is opened or closed, we introduce a pattern at the edge of the pocket that needs to be closed i.e. 'pocket edge' (see Figure 5b). This pattern interlaces the outermost warp thread of the top layer with the adjacent warp thread of the bottom layer, selectively closing this side. The single-sided pocket technique allows for the easy removal of components after weaving, which is essential for in-situ reprogramming, repairs, and washability. Pockets designed to house components must also consider yarn shrinkage after washing and be appropriately sized for the shape and topology of the components. Dimensions should take the component's height into consideration, in addition to its area ($L \times W$), to match their real-life sizes and ensure accurate pocket sizing.

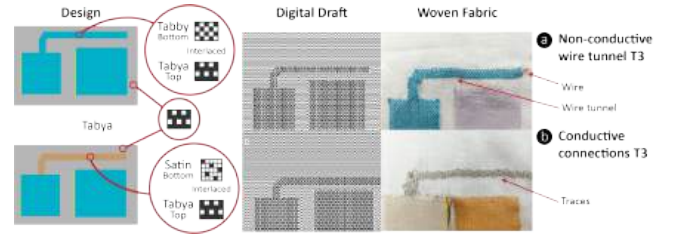


Figure 6: Woven electric connections using our WovenCircuits T3 method as: a) non-conductive wire tunnels and b) conductive thread traces.

3.3 T3: Weaving electric connections with conductive thread traces and wire tunnels

Connection traces (paths that link circuit components) can be created by weaving strands of conductive thread parallel to either the weft or the warp. However, this approach may not be suitable for implementing complex circuits. Our technique enables free-form connection traces, allowing them to continue horizontally, vertically, and/or diagonally as double weaving structures using either conductive or non-conductive thread. Using conductive thread, electrical connections can be visible from the front but insulated from the back side of the fabric. With non-conductive thread, the result is an insulated tunnel that can hide electrical wires (such as copper-core insulated wires), see Figure 6. Both options allow the conductive trace to be implemented in variable size, which can be used to optimize the electrical resistance of the path.

Horizontal Connections. We experimented with various methods of weaving conductive threads as traces and found that the first and last woven threads (entry and exit points) should be positioned

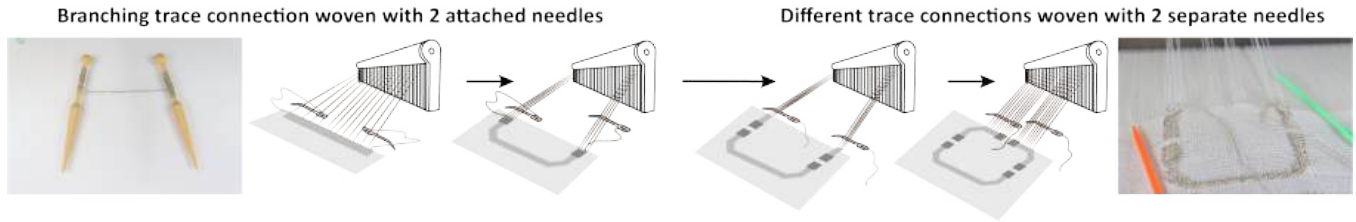


Figure 7: Weaving U-shaped (branching) traces with two attached needles versus isolated traces with two separate needles.

on opposite sides to effectively connect two distant components. To achieve this setup, horizontal traces must be designed with the height of an odd number of pixels (steps). However, since trace areas employ two layers, weaving steps are shared between both layers. For example, if a trace is designed with a height of 10 pixels, it will appear as 5 pixels in the conductive woven fabric. Therefore, to successfully weave a trace with a height of n steps, where n is an odd number, the trace must be designed with a height of $2n$ pixels.

Vertical Connections. Since weaving structures do not fill areas uniformly for each weaving row, we recommend choosing a weave structure without long floats for conductive vertical traces. A wider trace, such as 6 pixels wide, has proven to reduce the likelihood of failed steps during the weaving process.

U-shaped Connections. In our technique, we follow circuit design guidelines used in previous research [22] on spacing, crossings, and proper connection. For instance, we recommend weaving U-shaped traces using a single conductive thread, starting at the middle of the thread rather than with one loose end, preventing discontinuity. Since weaving progresses from the bottom up, the horizontal part of the U-shape should be woven first (starting from the middle of the thread), with two tapestry needles attached to the two ends, which are then woven in parallel after the branching occurs (Figure 7).

Crossing Traces. Crossing traces in circuit routing occurs when two or more traces intersect on the same layer, which can result in short circuits and cross-talk between different nodes [22]. This can be addressed in three ways: 1) redesign the circuit to avoid crossovers, 2) using insulation methods such as via stitching [34], or 3) employing multi-layer weaving structures. In the latter approach, the traces at the intersection can be insulated from one another by re-routing the traces to the top and bottom layers.

4 Characterization

To further understand WovenCircuits, we characterized the electrical behaviour and the washability effect of different woven structures. We conducted a series of experiments to quantify relevant design factors. The identified factors are the sizes of the traces designed (F1), the weaving structure drafted (F2), and the yarn ply or number of strands used in weaving (F3). These design factors enable customization at the three steps of the fabrication process, where they can be manipulated at Step 1 (designing the circuit), Step 2 (generating the weaving draft), and Step 3 (weaving the fabric circuit) respectively. Each of these factors can be broken down into both: 1-dimensional traces (lines, linear connection traces across weft),

2-dimensional traces (areas, regions of contact $L \times W$ or weft $\times$ warp), and the effects of laundering on resistance and dimensions of both.

Each experiment was repeated (i.e. re-woven with the same variables) five times for average measures and the resistance was measured using an AstroAI AM33D multimeter. Additionally, we followed established guidelines on the launderability of e-textiles [60] to inform our washing procedures in this experiment. All experiments were washed four times on a delicate cycle with cold water and no spin in a domestic washing machine (Samsung Electrolux NS28262). After each wash, when the fabric has completely air dried, we measured the resistance and trace dimensions under each design factor using the same multimeter. This characterization is to guide digital weaving practitioners and researchers of e-textiles with desired properties of customized woven circuits.

All experiments were woven on a 660 end (45 EPI) TC2 digital Jacquard loom. Both the warp and weft of the non-conductive woven fabric were implemented using 100% mercerized cotton 2/20. We tested several types of commercially available conductive threads, with silver-plated Madeira HC-12 ($<100\Omega/m$) providing the best functional and aesthetic effect [5]. Our results reflect these specific loom and warp settings; variations in these parameters may influence outcomes and should be considered in related applications.

4.1 F1: Designing Trace Sizes (at Step 1)

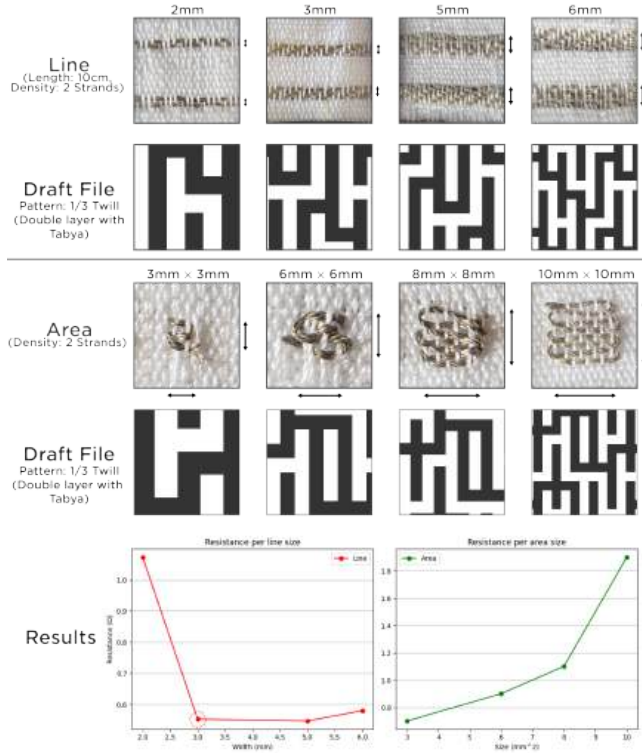
The dimensions of connection traces has a direct impact on the electrical performance of the circuit [4]. This design factor can be selected and altered during the design step. We conducted various trials to evaluate the performance of woven connection traces using our fabrication method, examining their effectiveness across both wefts (lines) and warps (area).

4.1.1 Line. To understand how trace size impacts conductivity, we compared the resistance of four line thicknesses. We wove 10 cm long traces, each with a distinct line thickness: 2 mm, 3 mm, 5 mm, and 6 mm. All traces were woven using a twill structure (Figure 8). Each line thickness was woven five times under the same conditions. The resistance of each trace was measured and documented, and the average resistance for each size was calculated (Table 1). The results indicated that at 3 mm thickness (and above), the average resistance drops and stabilizes around 0.55Ω . However, for traces thicker than 3 mm, the change in resistance is negligible, making the additional consumption of material and space unnecessary.

4.1.2 Area. Areas serve as the primary contact points between external elements and the rest of the connected traces. Therefore, increasing the size of area traces may be desirable for different applications. To understand the relationship between area size and

Table 1: Resistance Characterization: WovenCircuits woven with conductive thread in different sizes (F1).

Trace Type	Width/Size	Resistance (Ω)
Line (Length = 100 mm)	2 mm	1.07
	3 mm	0.55
	5 mm	0.54
	6 mm	0.58
Area	3×3 mm ²	0.71
	6×6 mm ²	0.94
	8×8 mm ²	1.13
	10×10 mm ²	1.91

**Figure 8: Resistance Characterization: The effect of trace size (F1) on electrical conductivity for line and area traces, highlighting the optimal balance between resistance and material-consumption (circled).**

conductivity, we measured the resistance of area traces of various sizes, ranging from 3×3 mm² to 10×10 mm² (see Table 1). We found that resistance is directly proportional to area size, meaning that larger area traces experience higher resistance values, as shown in Figure 8. Hence, it is recommended to design area traces with the smallest, most efficient sizes appropriate for the specific application.

4.1.3 Washability. The resistance of line traces steadily decreases after each wash, indicating a change in conductivity with repeated laundering. In contrast, the resistance of area traces decreases over the first two washes but remains consistent thereafter (Figure 9). For trace dimensions, both line and area traces maintain a consistent

length (no horizontal shrinkage) before and after washing. However, the height of the traces decreases by an average of 24% line traces and 12% for area traces after the first wash and then stabilizes, remaining relatively consistent with each subsequent wash.

**Figure 9: Washability Characterization: Trace size (F1) impact on resistance and dimensions (mm) after washing.**

4.2 F2: Drafting Weaving Structures (at Step 2)

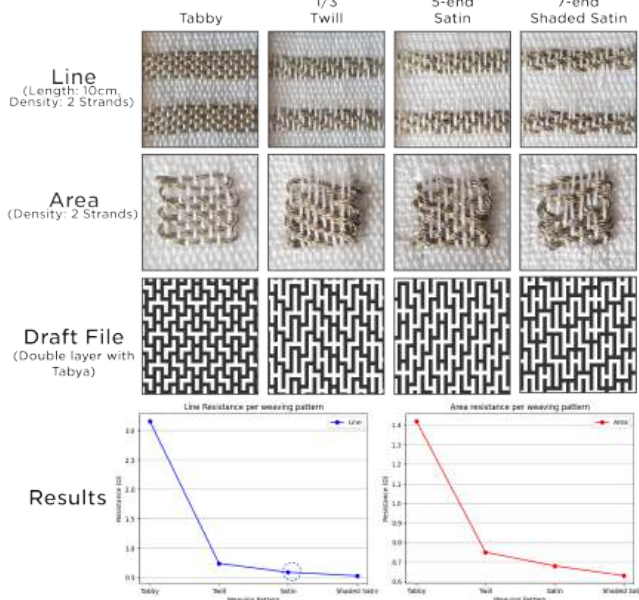
HCI researchers have explored various weaving structures to develop fabrics with integrated sensors [12]. This design factor can be modified during Step 2 (generating the weaving draft) without necessarily changing the visual design or affecting the weaving process on the loom. We characterized the effect of different weaving structures on electrical behaviour by testing four patterns: tabby, twill, satin, and shaded satin. Connection traces were woven using our fabrication method across both wefts (lines) and warps (areas).

4.2.1 Line. To characterize the weaving structures of line traces, we wove 10 cm long traces, each incorporating a unique weaving structure for the top layer of the Ghost Inlay, as shown in Figure 10. The plain weave structure (tabby), which has the least number of floats, exhibited the highest resistance value of 3.16 Ω due to its narrower spaces and tight interlacement. Comparatively, other weaving structures with a higher number of weft floats showed lower resistance values, with the satin exhibiting 0.59 Ω as the optimal compromise versus material consumption.

4.2.2 Area. To understand electric resistance of weaving structures in area traces, we drafted and wove square shapes of equal size 8×8 mm² using the four different patterns. Measurements were taken across the diagonal of each area trace, see Figure 10. The tabby pattern resulted in the highest measured resistance of 1.42 Ω , while resistance values steadily decline with patterns that have a higher number of floats, such as satin at 0.68 Ω and shaded-satin 0.63 Ω , as shown in Table 2. Since weaving with shaded satin is more material-demanding, we concluded that satin is the most efficient pattern for connection areas in terms of conductivity. However, for specific sensing applications, shaded satin might be preferred based on other factors, such as exposed surface area, which could take precedence over material consumption.

Table 2: Resistance Characterization: WovenCircuits woven with conductive thread with different weave structures (F2).

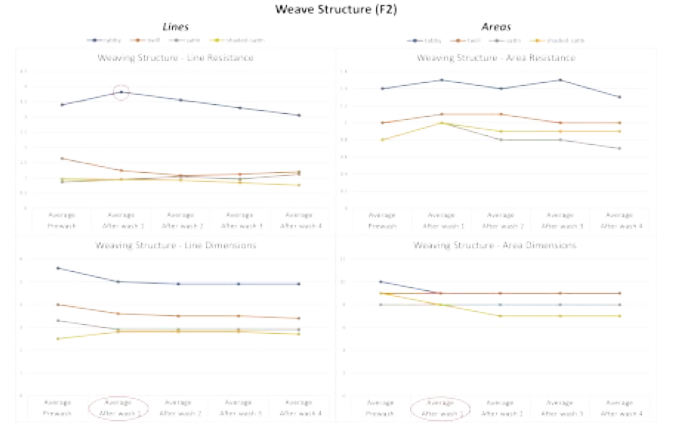
Trace Type	Weaving Structure	Resistance (Ω)
Line (Length = 100 mm)	Tabby	3.16
	1/3 Twill	0.74
	5-end Satin	0.593
	7-end Shaded Satin	0.58
Area	Tabby	1.42
	1/3 Twill	0.75
	5-end Satin	0.69
	7-end Shaded Satin	0.63

**Figure 10: Resistance Characterization: The effect of weaving structure (F2) on electrical conductivity for line and area traces, highlighting the optimal balance between resistance and material-consumption (circled).**

4.2.3 Washability. Overall, resistance decreases for both line and area traces after each wash (Figure 11). Most notably, the tabby structure exhibits a resistance that is, on average, 70% higher than other structures and decreases at a rate of 12%. This rate is significantly higher than the average decrease of 7% observed in other structures, rendering the latter almost negligible in comparison. Area traces follow a similar pattern but on a smaller scale. Additionally, the dimensions of both line and area traces remain stable in length, with no horizontal shrinkage, though the height decreases by 6% for area traces after the first wash and then stabilizes.

4.3 F3: Weaving with Multiple Strands (at Step 3)

Changing the cross-sectional area of the conductive thread can impact the trace conductivity. This design factor can be adjusted during the final step (weaving the fabric on the loom) without altering the visual design or draft files. To characterize the electrical

**Figure 11: Washability Characterization: Weave structure (F2) impact on resistance and dimensions (in mm) after washing.**

behaviour of WovenCircuits without changing the dimensions or structure of the designed trace, we compared the resistance of multiple stands of conductive thread across lines and area traces.

4.3.1 Line. We wove four line traces, each using a different number of strands of conductive thread (ranging from 1 to 4) until we reached saturation. The trace dimensions were fixed at $10 \times 0.5 \text{ cm}^2$, and the weaving structure consistently tabby, with each configuration repeated five times. Using a single strand of conductive thread resulted in the highest average resistance of 0.85Ω . However, resistance steadily decreased as we increase the number of strands, reaching 0.45Ω with the 4-strand conductive thread (Table 3). This reduction in resistance is due to an increase in the cross-sectional area of the conductive thread and the corresponding decrease in non-conductive spaces within the trace itself (Figure 12).

Table 3: Resistance Characterization: WovenCircuits woven with conductive thread with multiple strands (F3).

Trace Type	Width/Size	Strands	Resistance (Ω)
Line (L = 100 mm)	8 mm	1	0.853
		2	0.56
		3	0.48
		4	0.45
Area	$10 \times 10 \text{ mm}^2$	1	2.73
		2	1.50
		3	0.827
		4	0.813

4.3.2 Area. We compared the number of strands in a surface area by fixing the size at $8 \times 8 \text{ mm}^2$ (Figure 12). Similar to line traces, areas show a decrease in resistance with more strands, as fewer non-conductive spaces result in larger conductive cross-sections. The maximum resistance measured was 2.7Ω using one strand of Madeira HC-12, while the minimum resistance of 0.8Ω plateaus using 3 or more strands (Table 3). Hence, a 3-strand conductive thread can be considered the most efficient for area traces.

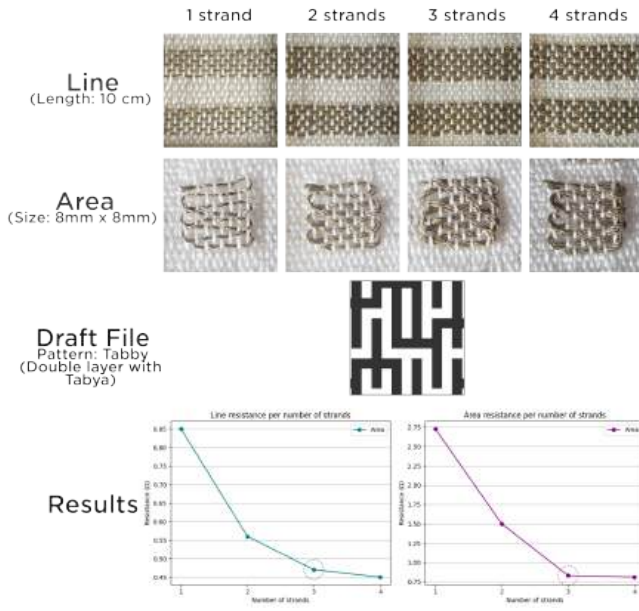


Figure 12: Resistance Characterization: The effect of number of strands (F3) on electrical conductivity for line and area traces, highlighting the optimal balance between resistance and material-consumption (circled)

4.3.3 Washability. The overall trace resistance decreases after each wash for both line and area traces (Figure 13). Similar to the observation made with using a tabby pattern, line traces made with a single strand have a higher average resistance compared to those using multiple strands, 58% on average, but their resistance decreases at a higher rate. Specifically, the resistance of single-strand line traces decreases at a rate of 20%, whereas the resistance of multi-strand traces decreases by an average of 4%. A similar pattern is observed with area traces. The dimensions remain consistent across the trace length, but the height decreases by 3% for area traces after the first wash, followed by consistent and steady measurements thereafter.

5 The 3-Step Fabrication Process

The main outcome of our Rtd exploration is understanding and reverse-engineering a step-by-step way of designing woven circuits. The process of fabricating WovenCircuits (presented below) is versatile and customizable, allowing designers to leverage the use of computational looms (i.e. digital prototyping weaving machines) utilized in recent work [16, 20, 83].

5.1 Step 1: Designing the circuit

The initial step involves designing the electric circuit, which includes determining the placement of each component with accurate dimensions and placeholder connections. In our work, we used an open-source circuit design CAD tool (DipTrace) with debugging capabilities to ensure thorough testing before weaving. The next step is to create a graphical representation of the circuit at a 1:1 scale, incorporating design factors such as colour (representing blocks of weaving structure), shapes, and dimensions of both functional and

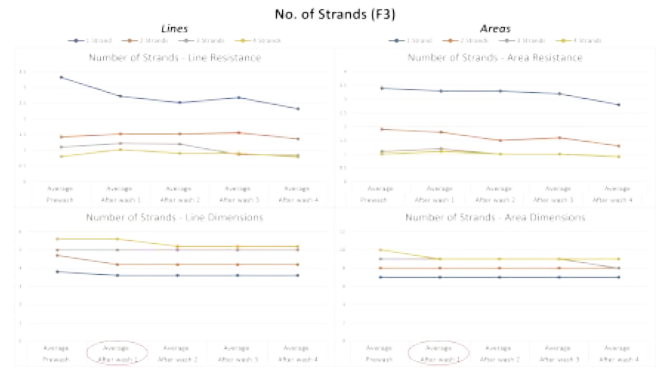


Figure 13: Washability Characterization: Yarn ply (F3) impact on resistance and dimensions (in mm) after washing.

aesthetic elements. Colours are used as markers for different areas, which will later be assigned specific weaving structures.

A vector design editor, such as Adobe Illustrator, is used to generate the required graphical representation with a translation of units between the design (PPI: pixels per inch) and woven structure (EPI: ends per inch). To avoid a stretched height caused by the aspect ratio asymmetry of different units, we found that dividing the design file height by 1.87% effectively squeezes it back to the expected dimensions. This step also requires weavers to decide on their selection of inert yarns, SETT (warp yarn per inch), and project warp yardage.

5.2 Step 2: Generating the weaving draft

A weaving draft file contains the information that a computational loom needs to produce the intended designs [16, 20]. We experimented with two approaches for producing weaving drafts: image-based and parametric-based. Creating image-based draft files for weaving (e.g., using Photoshop) has been widely adopted in the digital weaving community. However, recent parametric design tools (i.e. AdaCAD) [10, 12, 20] offer greater freedom, creative control, and customization over weaving structures. Nonetheless, a detailed description of the process and differences between both approaches is provided in the supplementary document. In this step, we apply weaving structures to colour-coded regions of the circuit by translating the vector design on Adobe Illustrator to the loom software using AdaCAD, see Figure 14. This is the step where we apply the WovenCircuits techniques described above, using them as a painting palette to decide where to apply Ghost Inlays (T1) and single-sided pockets (T2) while adhering to the considerations for weaving electric connections (T3).

5.3 Step 3: Weaving the fabric circuit

To weave the circuit design, simple shapes can be printed on paper for use on a floor loom, allowing WovenCircuits to be implemented as inlays using pick-up sticks. However, computational Jacquard looms can partially automate our fabrication techniques (T1, T2), reducing the time and labour required by up to 90%.

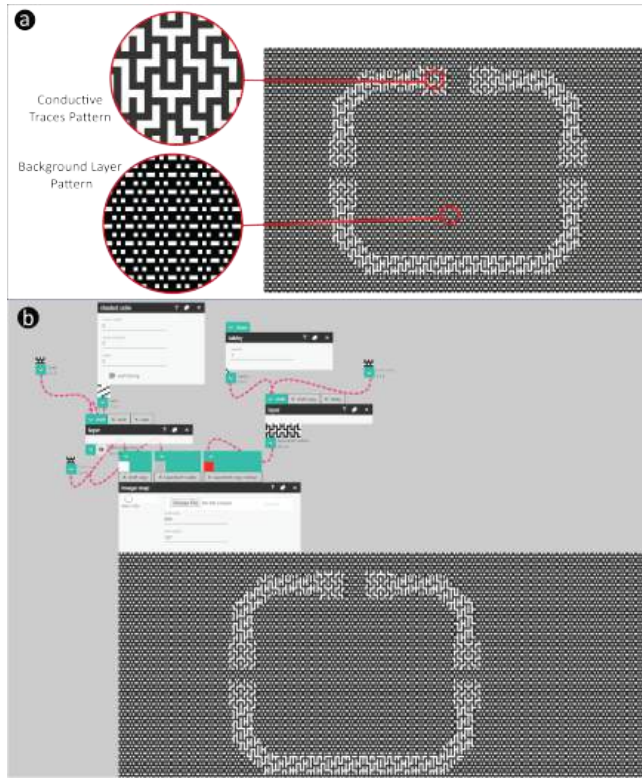


Figure 14: AdaCAD Design Environment: a) close-up on the resulting weaving structure, which compares background and ghost inlay layers; b) draft design process in AdaCAD.

6 Applications

One of the outcomes of our RtD [90] approach is a set of research artefacts built using our 3-step fabrication process and fabrication techniques. They feature a diverse range of designs, including wearable garments, bags, and everyday objects integrated into environments such as tables, chairs, floors, and walls.

6.1 PowerPocket Jeans for Wireless Charging

To demonstrate a fully woven circuit for wearable applications, we designed the PowerPocket as a woven wireless charger created using the T2 and T3 techniques. The PowerPocket is sewn onto the inside of the back pocket of a pair of jeans (see Figure 15). The user can charge their smartphone device by simply placing it in that pocket. The PowerPocket is woven with cotton 2/20 for warp and bottom layer weft, while the top layer weft is woven with cotton 8/20 in three different colours and Madeira HC-12 conductive thread for trace connections. Measuring 17×10 cm, the circuit incorporates a 5V 2A wireless transmitter coil. The insulated coil can deliver up to 10 watts of power to the receiving device. Power is supplied by a 3.7V LiPo battery, a Trinket microcontroller (22×15 mm), and a voltage step-up module (to 9V). We successfully tested the wireless charging functionality with an iPhone and Apple Watch. The circuit also includes a sewable power switch to give users control of when to turn it ON/OFF. All other components are easily removable

from their housing (T2) for laundering and maintenance. Users also have the flexibility to either proudly display their woven circuit as expressive clothing [47] or conceal it by attaching the denim pocket using snap fasteners. The latter preserves the look and feel of a typical everyday jeans back pocket, while adding extra functionality extending work on interactive pockets [77] and pants [69, 72].

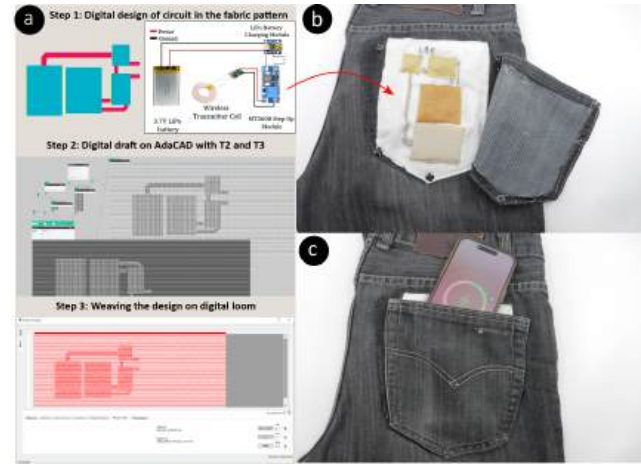


Figure 15: Design and implementation process for the PowerPocket Jeans showing the fabrication steps (a), the woven circuit (b), and wireless charging with an iPhone (c).

6.2 Thermo-Placemat for Warming Food

The Thermo-Placemat was created using the T1 technique of double-layer Ghost Inlays. This placemat, with dimensions 24×36 cm (cotton 2/20 for the warp and bottom layer weft), has the ability to keep food warm (see Figure 16). We wove Madeira HC-12 conductive thread into the bottom layer of the centre inlay pattern while using regular yarn for the top layer weft (cotton 8/20). We used a 4×4 twill weaving structure for the Ghost Inlay sections, alternating between two colours for visual appeal. For the background, we used the waffle-ish pattern to give the placemat a sturdy, durable drape with varying textures. We envision the Thermo-Placemat can be coupled with interactive tableware [61] and ceramic ware [87].

In addition to the conductive inlay, the user can choose to use the pocket created from the 2-layer structure to house an insulated heating pad to warm up their food. In both cases, this placemat can support playful and mindful interaction while engaging with food with or without utensils [42, 43].

6.3 GoGreen Backpack for Embodied Expression

To show a circuit hidden within woven fabric, we created a motif that integrates electric connections, input, and output in a completely discrete way. Inspired by efforts for climate action, the woven piece (17×36 cm) features a cityscape with buildings that light up dynamically, symbolizing the vibrancy of urban life (see Figure 17). When the woven bicycle icon is touched, the lights activate, highlighting the importance of sustainable eco-friendly

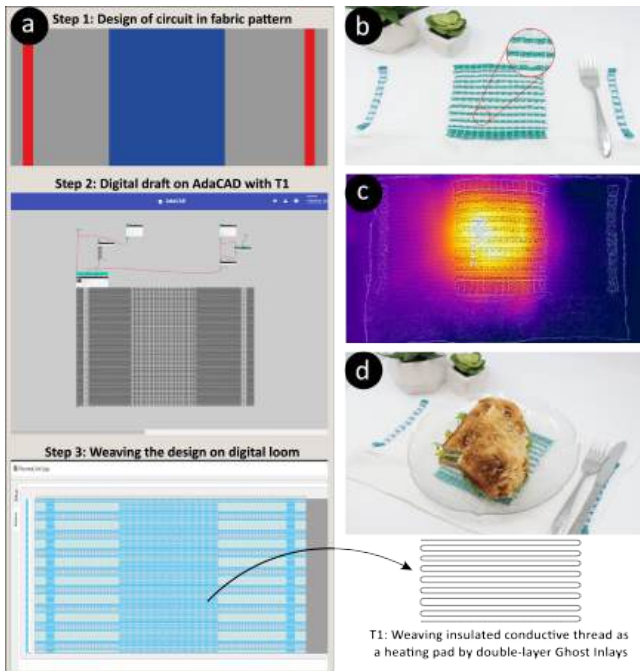


Figure 16: Design and implementation process for the Thermo-Placemat showing the fabrication steps (a), the woven circuit (b), a heat map image (c), and the placemat keeping a plate of food warm (d).

transportation in combating climate change. After weaving the fabric circuit, we used it to sew an external compartment on a backpack that allows users to be proud of their supported cause. This application uses the T1 and T2 techniques of weaving insulated electrodes and pockets for housing the sewable battery holder.

We wove Madeira HC-12 conductive thread in the bottom of the center inlay (featuring the bicycle as the key design element) for a pressure sensor acting as the circuit switch. We used a tabby weaving structure for the line traces (T1) and pockets (T2) and integrated seven sewable LEDs in some ‘windows’ to light up as the photonic actuation. The piece is also woven in a two-pick structure, a weaving technique where two picks are inserted between each row of warp [70], facilitating the creation of multi-colour designs by alternating between shuttles repeatedly for each top-layer weaving step. We used a cotton 2/20 warp and cotton 8/20 yarn for both the top and bottom layer weft. We envision this design can be used for functional purposes (such as lighting up a dark backpack compartment when opened up to help see inside) or experiential purposes (such as self-expression and raising public awareness [25] with its actuation effect [50]).

6.4 VibroChair Cover for Posture-Correction

WovenCircuits can be designed in curves and aesthetic shapes beyond geometrical straight lines like a PCB. To explore this, we created a fabric chair cover, that can be retrofitted for existing office or dining furniture, with WovenCircuits seamlessly integrating a complete circuit (see Figure 18). This application helps users correct

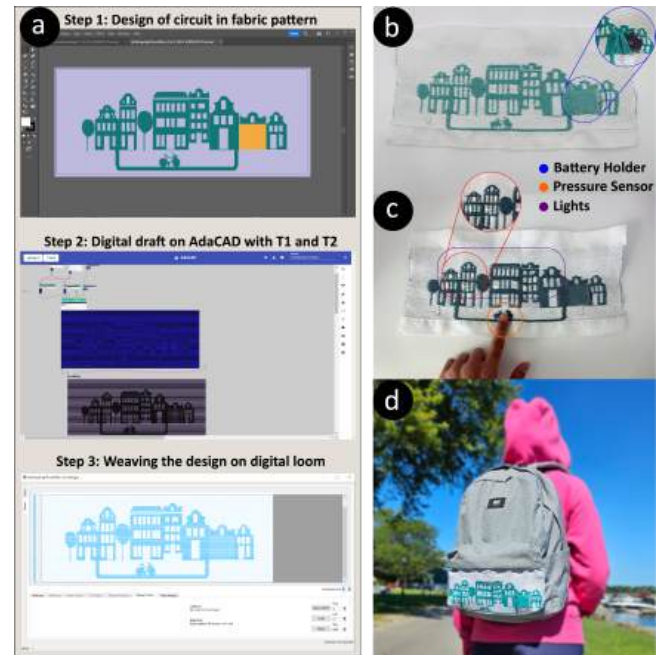


Figure 17: Design and implementation process for the GoGreen Backpack showing the fabrication steps (a), the woven circuit (b), the interaction with the bicycle pressure sensor, lighting up the city scene (c), and the backpack being worn in-the-wild (d).

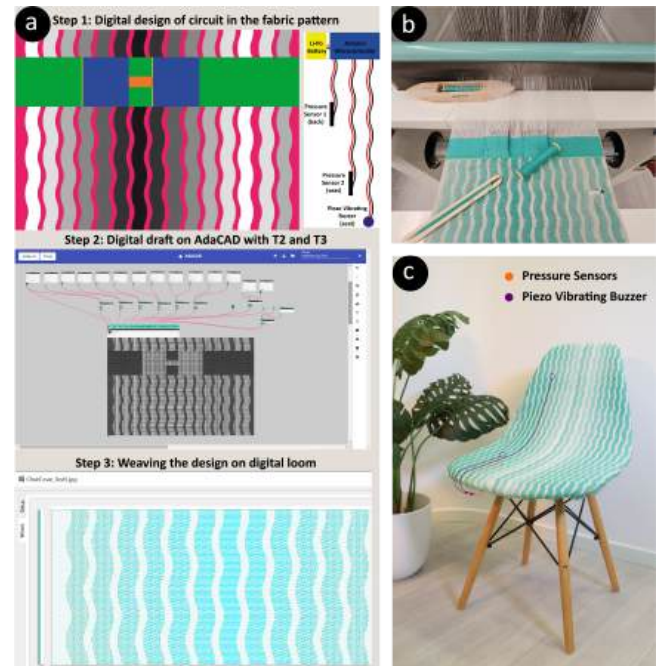


Figure 18: Design and implementation process for the VibroChair showing the fabrication steps (a), the fabric as it is being woven (b), and the VibroChair (c).

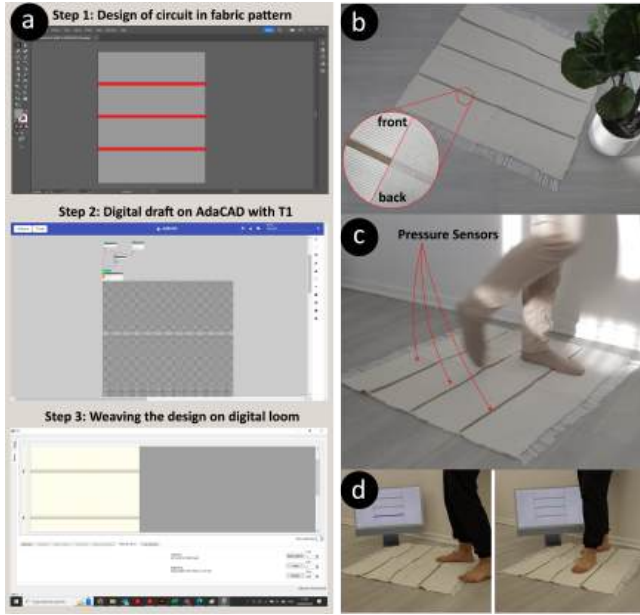


Figure 19: Design and implementation process for the SensingRug showing the fabrication steps (a), the woven rug (b, c), and the interaction with the SensingRug (d).

their posture by subtly alerting them when they are not sitting up straight [45]. Our VibroChair Cover comprises two woven fabrics (80×36 cm each) sewn together across the salvage seam and sewn to an elastic chair cover for easy removal. It uses the T3 technique of weaving wire tunnels to conceal (in curvy waves) two pressure sensing spots and a small piezo motor that vibrates when sitting is detected with no leaning back. We used cotton 2/20 yarn for the warp, cotton 8/20 yarn for the top and bottom layer weft, and Madeira HC-12 conductive thread for all connections. Our application also uses T2 to integrate the microcontroller and battery into single-sided pockets at the edge of the fabric. This extends previous work on interactive chairs [6, 62] where electronic components are attached to the chair itself, allowing the customization of woven chair covers to add interactivity to existing chairs.

6.5 SensingRug for Activity Recognition

WovenCircuits can be integrated within everyday fabric objects around homes to capture natural interactions people are performing in their daily lives. For example, we can detect when a user sits on a sofa or bed for too long or steps or falls on the floor carpet. To explore this, we created the SensingRug, a 70×75 cm rug with tassels that can detect when someone steps on different parts of it. This application resembles a Boho handwoven bathroom rug or floor mat featuring natural colours (weight 5 weft yarn). We used WovenCircuits to seamlessly integrate a sensor circuit (see Figure 19). Our SensingRug uses the T1 technique of weaving insulated electrodes (with Ghost Inlays) in three touch sensing spots, detecting when users step on it (barefoot or with socks on). These touch points are made with Madeira HC-12 conductive thread on the top layer weft

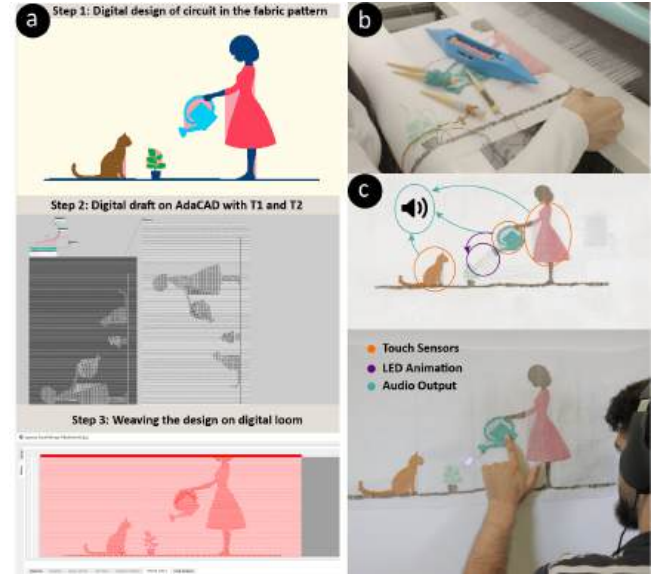


Figure 20: Design and implementation process for the ThirstyCat Tapestry showing the fabrication steps (a), the woven circuit (b, c), and the interaction with the tapestry (d).

and cotton 8/20 yarn for the bottom layer weft. This extends previous work on woven sensor swatches [12] where a full-scale object is fabricated with the sensors, allowing the seamless integration of interactivity into real-world things.

6.6 ThirstyCat Tapestry for Tactile Animation

To explore woven tapestries, we designed an interactive one that combines auditory and visual experiences with tactile interactions. Our wall-mounted tapestry measures at 36×97 cm and features a story scene with three main characters (interactive elements): a woman, a cat, and a watering can for a potted plant (see Figure 20). Each of these elements functions as a capacitive touch sensor, woven using the T1 and T2 techniques and connected to a Bare Conductive Touch Board microcontroller. When the user interacts with these elements, the tapestry animates the sewable LEDs and responds with audio outputs such as a meow, waterfall sound, and the speaking woman's voice. We applied the T1 double weaving technique with Ghost Inlays to the three elements in conjunction with T2. Additionally, we inverted the pattern for 'background' areas around the elements to workaround the branching-out curves. For example, the watering can was colour-coded as a block with two colours: pink for the background colour and blue for the watering can, as seen in Figure 20. We applied the inverse of the T1 pattern to the pink areas so that the background colour (white) would rise to the surface, allowing the watering can to be woven with a single continuous conductive thread. This approach avoids segmenting the sensor into disconnected sections, ensuring it functions as one piece. We discuss this workaround more thoroughly in Section 7.6. This tapestry was inspired by previous work on woven interactive tapestries [13] and e-textile storytelling crafting [71]. The tapestry uses cotton 2/20 for the warp and bottom layer weft, while the

top layer weft was woven with textured and coloured conductive yarns [33] made from cotton 8/20 yarn.

7 Discussion, Limitations, and Future Work

Below, we reflect on the unique properties of WovenCircuits through a qualitative perspective of its contribution, comparing it to and building on existing literature.

7.1 Customizability of Circuit Design

Through iterative experimentation, WovenCircuits demonstrates how weaving can move beyond grid-like, rectangular patterns [46, 79] toward supporting free-form shapes, offering greater expressiveness and customization. The scalability of conductivity (F2), exemplified in high-power applications such as the PowerPocket Jeans, shows the ability to support power-intensive circuits. Inlay and double weaving methods [13] incorporate non-conductive threads in parallel with conductive ones, creating gaps that degrade conductivity. By isolating electrodes within a non-conductive layer, WovenCircuits eliminates these gaps, significantly enhancing conductivity and reducing overheating and fire risks. These findings extend the landscape of woven interface design by offering an approach to woven circuit fabrication that is reproducible and invites designers and researchers to experiment with textile-based interactivity in everyday contexts.

Furthermore, literature on woven interfaces [88] has called for further hardware integration for seamless woven circuitry, suggesting approaches like flexible PCBs tailored to 2D shapes. While WovenCircuits demonstrates prototyping capabilities for integrated circuits, bulky components (e.g., motors or screens) remain a limitation shared across similar interfaces [88].

7.2 Automation and Control of Woven Circuit Fabrication

Previous work [46] highlighted the value of using digital Jacquard looms to automate the process of creating woven interfaces, providing advantages in scalability, customizability, and speed. This approach also empowered designers with creative freedom to hide or reveal circuit components within expressive and aesthetic designs. Additionally, it enabled scaling up final designs, building on the literature’s knowledge of weaving swatches [9, 29, 53], patches [8, 46], and bits [37, 38] for broader usage.

Computational design and digital looms offer selective control over warp ends, eliminating constraints related to pattern implementation. However, the current digital weaving process is not optimized for e-textile applications. WovenCircuits was explored to capitalize on the advantages offered by computational looms, which are increasingly prevalent in the HCI and maker communities. State-of-the-art digital weaving machines, such as the TC2 [66], can be expensive (starting at €12,000 for a 1-Wide), but still within the range of small laser cutters. This enables both creative experimentation by hand and scalable manufacturing [79]. Our 3-step process relies on existing software tools [16, 20] that can be expanded to better support circuit design and their weaving drafts. This approach fosters expressive creations during the crafting process, where adjustments can be made on-the-fly in any of the three steps. Similarly, the three design factors can be altered throughout

the process, allowing creators to intuitively make customizations, similar to the flexibility found in sewing practices.

As a form of co-production [15, 20] between the maker, machine, tools, and materials, we sought to streamline the process for makers of all skill levels, from novice to expert. By offloading production to the machine, makers can instead focus their efforts on using digital tools to create personalized, unique designs. This approach builds on previous work that encourages engagement with hand tools, such as using tapestry-needles as shuttles [79]. To make our work more accessible and replicable, we also opted to make use of off-the-shelf sewable e-textile components (such as sewable LEDs, battery holders, and microcontrollers) that are typically used for hand-stitching e-textile circuits. In contrast, using custom PCBs would sacrifice replicability and accessibility to maker communities.

7.3 Supporting High-Fidelity Prototyping

Low-fidelity prototyping, characterized by rapid swatch fabrication, has been a focus of e-textile research [13, 73, 88], where deployable applications have not received the same level of attention [31]. In contrast, our work advances high-fidelity prototyping, enabling the creation of fully integrated woven circuits. Our range of potential applications showcases high-fidelity research products that not only support functional interaction (sensing and/or actuation) but also embellish everyday things and expressivity.

While previous work recommends early-phase, low-fidelity prototyping processes in e-textile design to create “many swatches” that are “rapidly fabricated” [89], our approach supports high-fidelity prototyping of complete woven circuits. Our sample applications demonstrate the capabilities of WovenCircuits, which can be used in combination with other tools, such as Loom Pedals [83], or sustainable materials like EcoThreads [89] and BioFibers [49] for creating biodegradable woven e-textiles. This shift to high-fidelity prototyping repositions e-textiles from temporary research products to fully realized design artefacts.

Future research could also benefit from exploring applications of WovenCircuits in the context of 3D weaving [27, 81, 82], with the opportunity of interdisciplinary collaboration with 3D weaving specialists. While WovenCircuits is demonstrated through 2D Jacquard weaving, 3D weaving techniques could unlock new structural opportunities in woven circuits and shape-changing textiles. However, current 3D weaving techniques require the use of industrial Jacquard looms [27], which are often inaccessible to many makerspaces in comparison to smaller scale digital Jacquard looms like the one we used to synthesize this work. This creates an opportunity for researchers, designers, and practitioners to use the WovenCircuits 3-step workflow, and potentially its techniques (T1–T3), to explore other weaving applications such as 3D weaving.

7.4 Connectors as Functional and Aesthetic Elements in Woven E-textiles

Circuit components, such as connection traces and microcontrollers, have been identified by previous work [41, 79, 88] as a “limitation of [woven] interface research” due to their rigid nature. Inspired by research on creating woven circuits as “2D shapes that align with the design” [88] and distributing electronic components into

“circuit islands” [79], WovenCircuits enables seamless integration of these components into the fabric’s structure.

Previous work focused on weaving the sensing or actuating elements while rigid components are treated as external parts [12, 78, 84], often relying on extended wires for connections [30, 31, 58], or using components with a small footprint [30, 31] and employing weaving structures to hide components [13, 51]. However, WovenCircuits integrates circuit components directly into the fabric and offers flexibility in terms of weave structure, yarn type, weight, and colour for the top layer of component pockets. Unlike other approaches, WovenCircuits treats connections as integral aesthetic elements without compromising functionality.

7.5 Washability, Durability, and Longevity

To ensure the longevity of woven circuits, rigid off-the-shelf components can be designed for easy removal. By using conductive snaps [9, 11] to connect components to traces, users can remove them for maintenance and washability [7]. Additionally, weather-specific limitations, such as exposure to moisture, UV light, and extreme temperatures, pose challenges for integrating woven circuits into outdoor furniture and outerwear. Addressing these limitations will require further research into protective coatings, insulation methods, and weather-resistant materials to ensure that woven circuits maintain functionality in diverse conditions.

In Section 4, we conducted experiments on WovenCircuits’ performance in terms of washability and shrinkage by measuring resistance and dimensions under lab-controlled conditions. However, we have not assessed its stretchability and durability under daily use, where extensive friction and flexing may impact performance. A more comprehensive “in-the-wild” evaluation may be required to fully understand the real-world performance of these woven circuits, particularly in long-term, high-use scenarios.

7.6 Restrictions of Weaving Curved Shapes

WovenCircuits can support the weaving of complex shapes, such as U-shaped traces (see Figure 7). By using tapestry needles at both ends of a single conductive thread and weaving from the middle of the thread until the point where the U-shape branches, we effectively preserve the continuity of the trace. However, WovenCircuits does not accommodate weaving inverted U-shaped traces that start from two branches and then join i.e. non-continuous shapes. These are shapes with two regions sharing the same weaving step but are separated by a gap across the weft. Similar to how shapes can branch out during the weaving process, as with U-shaped traces, two branches may also merge to form a single continuous region. For non-conductive areas, weavers can simply add an additional tapestry needle for each branch. However, in conductive regions, introducing a second conductive-threaded needle for each branch would result in discontinuities, compromising the electrical connection and potentially causing functional errors in the circuit. While we worked around this limitation in the ThirstyCat Tapestry by inverting the T2 draft in the “background” areas where the branches merge (see Figure 20), this solution has drawbacks. Because the draft is inverted, the conductive trace surfaces on the bottom layer of the fabric, leaving the trace uninsulated. As a result, this approach may not be suitable for wearable applications.

8 Conclusion

Weaving presents an opportunity for extended forms of hybrid crafts [1, 36, 52, 79, 87], allowing researchers and makers to seamlessly integrate functionality and aesthetics into everyday things in the home and on the body. Adopting an RtD [90] approach, we explored WovenCircuits to address this gap by investigating the underexplored area of connection traces and introducing fabrication techniques that enable insulating electrodes (T1), housing electrical components (T2), and weaving electrical connections (T3). This approach enables larger-scale, real-world applications, building on the knowledge on swatches [9, 29, 53], patches [8, 46], and bits [37, 38], and supporting more complex and functional e-textile designs.

Our work also provides a detailed exploration of key design factors influencing the performance of woven circuits, such as trace dimensions (F1), weaving structure (F2), and number of conductive thread strands (F3). Our experiments evaluated the conductivity of linear traces as well as areas for pad connections before and after several washes. Results show that resistance measurements remain relatively stable after the second laundering trial, with shrinkage levels exhibiting the expected behaviour of other regular woven cotton (i.e. 7% average vertical shrinkage for area traces across the three design factors and no horizontal shrinkage). Results also show that 3 strands and ‘satin’ weaving structure exhibits the best trade-off/balance between resistance and amount of conductive thread used for weaving linear and connection areas.

Through the set of fully functional research products, we demonstrate the diversity of applications that WovenCircuits can help create (wearables, objects on the go, on tables, chairs, floors, and walls). Finally, we offer a discussion on the design opportunities and limitations to guide future researchers and practitioners in creating complete and scalable woven circuits.

Acknowledgments

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